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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/622,467	07/21/2003	Pil Heon Choi	HI-0169	4533
34610 75	590 05/17/2006		EXAM	INER
FLESHNER & KIM, LLP			TRAN, QUOC DUC	
P.O. BOX 2212 CHANTILLY,			ART UNIT	PAPER NUMBER
			2614	
			DATE MAILED: 05/17/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Comment	10/622,467	CHOI, PIL HEON				
Office Action Summary	Examiner	Art Unit				
	Quoc D. Tran	2614				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 24 Fe	ebruary 2006.					
	action is non-final.					
3) Since this application is in condition for allowan	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>1-24</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5)⊠ Claim(s) <u>8-24</u> is/are allowed.						
6)⊠ Claim(s) <u>1 and 4-7</u> is/are rejected.						
7) Claim(s) 2 and 3 is/are objected to.						
8) Claim(s) are subject to restriction and/or	election requirement.					
Application Papers						
9)☐ The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
See the attached detailed Office action for a list of	or the certified copies not re	eceived.				
Attachmont(c)						
Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)		Mail Date				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	5) Notice of Info	ormal Patent Application (PTO-152)				

DETAILED ACTION

Response to Amendment

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 4-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Fallon et al (6,134,308).

Consider claim 1, Fallon et al teach a Private Branch Exchange (PBX) apparatus for transmitting and receiving CID (Caller ID) (abstract; col. 4 lines 5-7) comprising: an Analog Trunk Convergency (ATC) unit for converging with an exchange in the PBX connected to the exchange (col. 4 lines 2-5); a Subscriber Line Convergency (SLC) unit for transmitting CID and/or data by converging with a subscriber line connected to each port (col. 4 lines 17-29); a control block for controlling a CID service for the analog trunk and the subscriber line units; a data path control block for controlling data transmission through a data path between the ATC unit and the SLC unit (col. 4 lines 8-12); a CID service unit including CID and signal detecting unit and a CID and signal transmitting unit that each perform digital processing on the CIDs and signals; a signal transmitting/detecting block for performing signal transmitting and/or detecting through the data path (col. 5 lines 2-16); and a switching block for connecting the data path between the CID service unit and the ATC and SLC unit, and for selectively switching the data

path between the signal transmitting/detecting block and the CID service unit (col. 4 lines 30-50).

Consider claim 4, Fallon et al teach an apparatus in a Private Branch Exchange (PBX) for transmitting and receiving a Caller ID (CID) (abstract; col. 4 lines 5-7) comprising: a CID detecting block for detecting system signal and/or the CID received through a highway as a data path connected by a switching block (col. 5 lines 2-16), and storing the signal and/or the CID in a corresponding area per port inside a CID detection memory block (col. 5 lines 17-28); a CID transmitting block for transmitting the CID to an affected receiver terminal through the highway as the data path; a CID detection memory for assigning a memory area to each of subscriber ports of the SLC unit and storing the signal and/or the CID for a corresponding port; a CID transmitting memory for storing a system signal and/or a CID in each port, in order to transmit a predetermined CID to an affected receiver terminal when a ring signal is transmitted to the affected receiver terminal; and a local control block for controlling CID transmission to a corresponding port in the SLC unit through a system bus by reading the signal and/or the CID of each port from the CID detection memory (col. 4 lines 7-29).

Consider claim 5, Fallon et al teach wherein the CID detecting block comprises: a highway convergency block for receiving the signal and/or the CID by converging with the data path connected by the switching block; a CID detecting block for detecting the CID received from the highway convergency block; a system signal detecting block for detecting a system signal transmitted to the highway convergency block (col. 4 lines 30-50); and a memory interface block for interfacing with the CID detection memory, to store the CID detected by the

CID detecting block and the signal detected by the system signal detecting block in a predetermined memory area of a corresponding port (col. 5 lines 17-28).

Consider claim 6, Fallon et al teach wherein the CID transmitting block comprises: a memory interface block for interfacing CID that is transmitted from the CID transmitting memory; a CID transmitting block for transmitting the CID from the CID transmitting memory to a highway convergency block, in order to transmit the CID to the switching block; a system signal transmitting block for transmitting the system signal received from the CID transmitting memory; and a highway convergency block for transmitting the signal and/or the CID by converging with the highway as the data path connected to the switching block (col. 5 lines 17-28).

Consider claim 7, Fallon et al teach a method for transmitting and receiving a Caller ID (CID) in a Private Branch Exchange (PBX) (abstract; col. 4 lines 5-7), the method comprising: establishing a line with the PBX through a general switched telephone network (col. 4 line 2-5); converting a received data through the line, and storing the data through a switching block in a CID service unit comprised of at least one memory (col. 4 lines 11-29); and transmitting all or part of the stored CID to a terminal, through the switching block and/or a Subscriber Line Convergency (SLC) block and display the CID on the terminal (col. 5 line 54 – col. 6 line 15).

Allowable Subject Matter

- 3. Claims 2-3 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 4. Claims 8-24 are allowed.

Response to Arguments

5. Applicant's arguments with respect to claims 1-24 have been considered but are moot in view of the new ground(s) of rejection.

Important Notice

6. The Group and/or Art Unit location of your application in the PTO has changed. To aid in correlating any papers for this application, all further correspondence regarding this application should be directed to *Group Art Unit 2614*.

Conclusion

- 7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 8. Any response to this action should be mailed to:

Mail Stop _____(explanation, e.g., Amendment or After-final, etc.)
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Facsimile responses should be faxed to:

(571) 273-8300

Hand-delivered responses should be brought to: Customer Service Window Randolph Building

401 Dulany Street Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Quoc Tran** whose telephone number is (571) 272-7511. The examiner can normally be reached on M, T, TH and Friday from 8:00 to 6:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Curtis Kuntz**, can be reached on (571) 272-7499.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the **Technology Center 2600** whose telephone number is (571) 272-2600.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

QUOCTRAN PRIMARY EXAMINER

AU 2614

May 12, 2006